



PATENT / DOCKET NO.: 24061.118 / TSMC2003-0654
CUSTOMER NO.: 27683

**INVENTOR'S DECLARATION FOR
PATENT APPLICATION**

As below named inventors, we hereby declare that:

Our residence, post office address and citizenship are as stated below next to our names;

We believe we are the original, first and sole joint inventors of the subject matter which is claimed and for which a patent is sought on the invention entitled

**A METHOD FOR TARGET QUEUE TIME CALCULATION
IN SEMICONDUCTOR MANUFACTURING PLANNING**

the specification of which: (check one)

 X is attached hereto.
 was filed on _____
under Attorney's Docket Number _____
as Application Serial No. _____
and was amended on _____ (if applicable).

We hereby state that we have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

We acknowledge the duty to disclose information which is material to the patentability of this application in accordance with 37 CFR 1.56, including for continuation-in-part applications, material information which became available between the filing date of the prior application and the national or PCT international filing date of the continuation-in-part application.

We hereby declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 U.S.C 1001 and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

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R-59526.1

In re application of:	§	Attorney Docket No.:	24061.118
Chen et al.	§		TSMC2003-0654
	§		
Serial No.: Unknown	§	Customer No. 27683	
	§		
Filed: Herewith	§	Group Art Unit: Unknown	
	§		
For: A METHOD FOR TARGET QUEUE	§	Examiner: Unknown	
TIME CALCULATION IN	§		
SEMICONDUCTOR MANUFACTURING	§		
PLANNING	§		

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